

**EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with H. J. Staas on March 3, 2008.

The application has been amended as follows:

In claim 3, line 2, the phrase "said execution rate" is changed to "an execution rate".

In claim 5, line 2, the phrase "a divided clock signal" is changed to "an output clock signal".

In claim 5, line <sup>2-3</sup>3, the phrase "said divided clock signal" is changed to "said output clock signal".

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In claim 5, lines 8-9, the phrase "proceeds forward/behind" is changed to "proceeds forward/recedes behind".

Claim 7 (New) A method for synchronizing a phase of an output clock signal with a phase of an input clock signal wherein said output clock signal is generated by dividing a master clock signal, comprising: